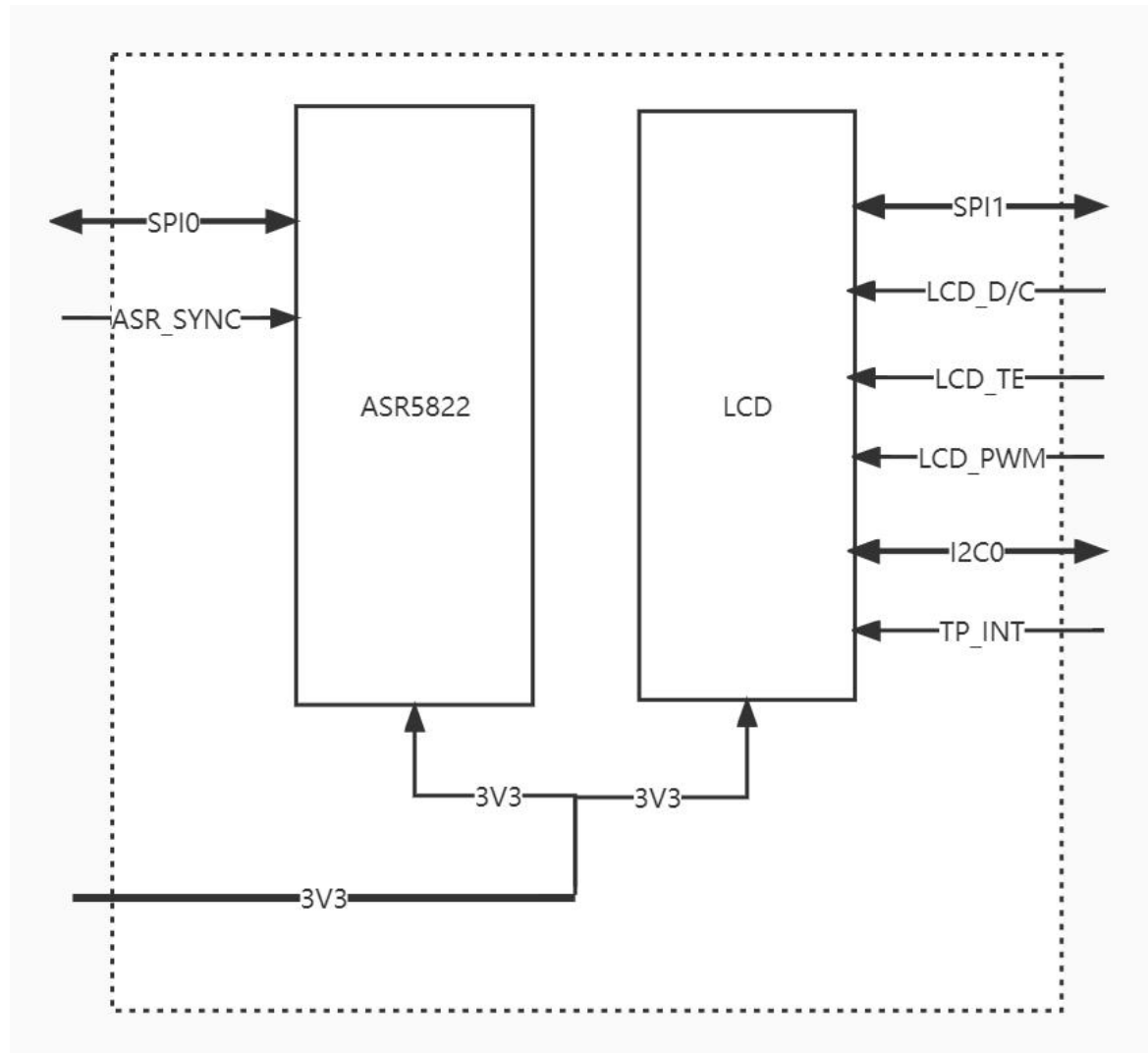




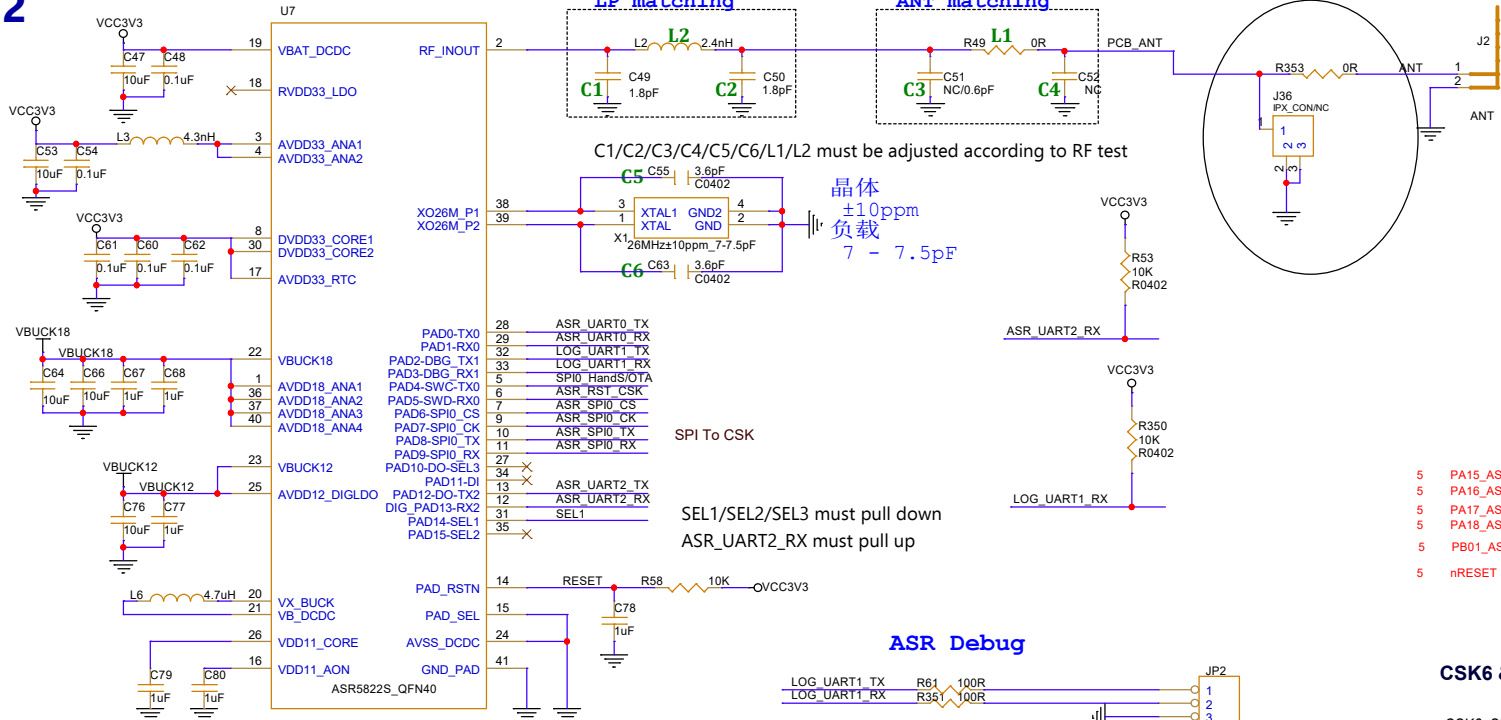
RD\_Nanokit\_ASR\_LCD  
Schematic Revision 1.0

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 <b>LISTENAI</b>	<b>RD_CSK6012_USB_TEST_V1_0</b>		
	Title: <b>PAGE02_SYSTEM-AECHITECTURE</b>		
	Size <b>A3</b>	Document Number <b>1</b>	Rev <b>V1.0</b>
	Designer:	Date: <b>Thursday, December 29, 2022</b>	Sheet <b>2</b> of <b>6</b>

# ASR5822



PIN24 AVSS\_DCDC  
单点接地不要直接和芯片下方地焊盘连在一起;

- 5 PA15\_ASR\_SPI0\_CLK << A\_15-RX/spi0\_clk
- 5 PA16\_ASR\_SPI0\_CS\_N << A\_16/spi0\_cs\_n
- 5 PA17\_ASR\_SPI0\_MISO << A\_17/spi0\_miso
- 5 PA18\_ASR\_SPI0\_MOSI << A\_18-TX/spi0\_mosi
- 5 PB01\_ASR\_SYNC << B\_01/spi0\_sync2
- 5 nRESET << nRESET

## CSK6 & ASR CONNECTION

CSK6 SPI to ASR (CSK6 Slave ASR Master)

ASR_SPI0_CS	R2	22R	R0402	A_16/spi0_cs_n
ASR_SPI0_CK	R3	22R	R0402	A_15-RX/spi0_clk
ASR_SPI0_TX	R5	22R	R0402	A_18-TX/spi0_mosi
ASR_SPI0_RX	R6	22R	R0402	A_17/spi0_miso
SPI0_Hands/OTA	R7	22R	R0402	B_01/spi0_sync2

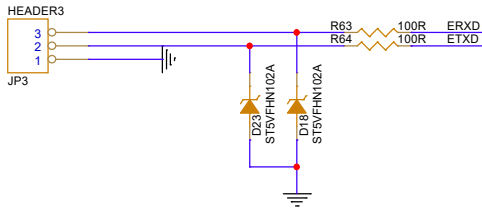
CSK6 UART TO ASR

ASR_UART2_TX	R75	22R	R0402	A_15-RX/spi0_clk
ASR_UART2_RX	R76	22R	R0402	A_18-TX/spi0_mosi

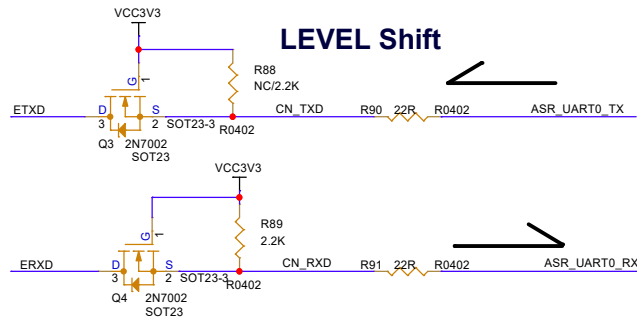
CSK6-reset From ASR

ASR_RST_CSK	R18	22R	R0402	nRESET
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## PWR/UART CON

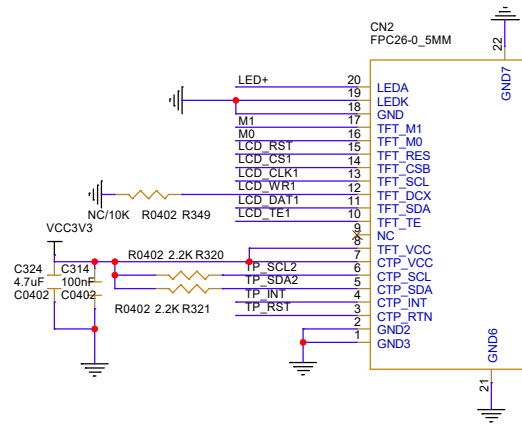
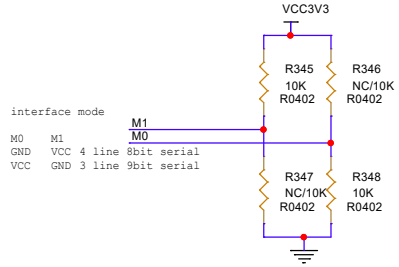
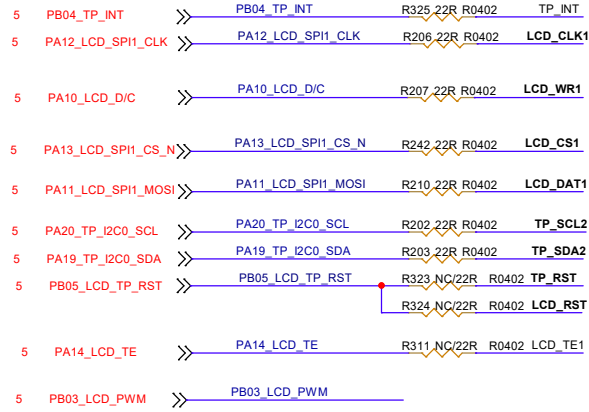


## LEVEL Shift



Note: 3.3->5V level-shift, 5V side of Host-board must pull up to 5V use resistance.

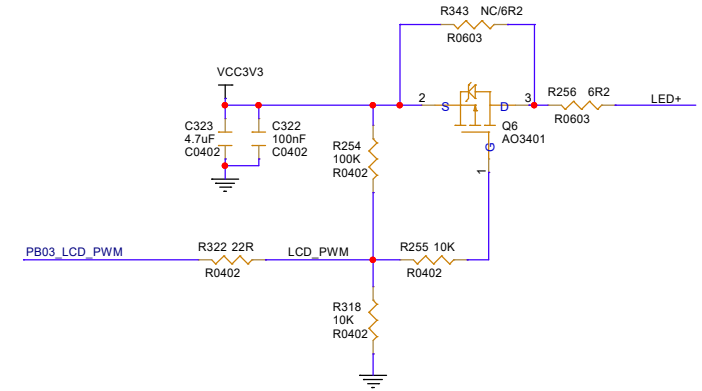
### LCD+TP:



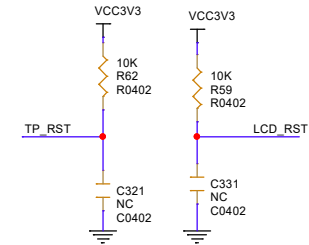
说明: LCD Driver IC : ST7789V  
TP Driver IC : FT6336V

注意: 需要LCD屏支持SPI串口方式, 同时LCD屏需要接TE  
LCD这里接口可根据客户实际生产用做调整, 图示仅作参考  
优先有TE接口的LCD。

### PWR LED:

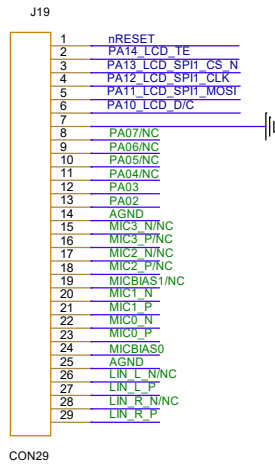
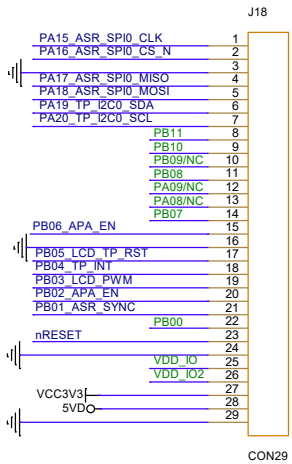
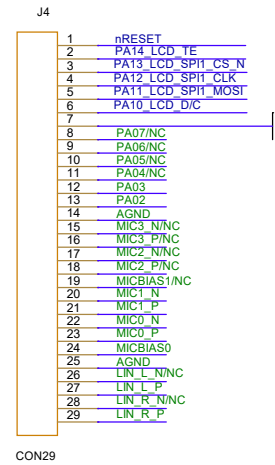
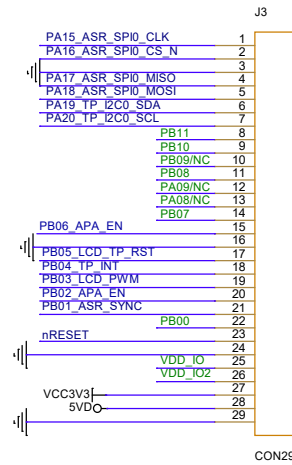
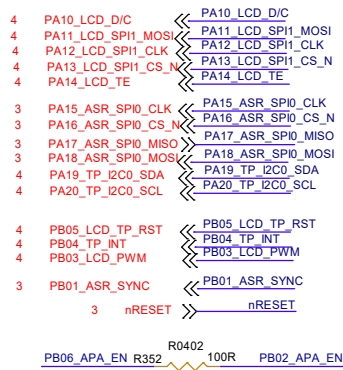


### TP\_RST&LCD\_RST



# INTERFACE

## PIN\_CONNECTOR



	RD_Nanokit_ASR_LCD_V1_0			
	Title PAGE05_INTERFACE			
Size A3	Document Number 1	Rev V1.0		
Designer:	Date: Wednesday, February 08, 2023	Sheet 6	of 6	

Date	Revision	Change
Dec 19 2022	Rev 1.0	1. First Revision
2022-2-8	Rev 1.1	1.CN2屏接口PIN定义对调, PCB接口封装采用上接口FPC座