Date	Version	Owner	Description
8/30/2022	V1.0	Daniel Liu	Initial version

	封装						场景1：可视门铃	场景2：家电面板	场景3：扫读笔	场景4：WiFi摄像头	场景5：语音WiFi模块	场景7：通道芯片
	封装A：QFN76 全功能，SIP PSRAM						QFN76	QFN76	QFN60	QFN52	QFN40	QFN40
	封装B：QFN60 扫读笔，SIP PSRAM						41GPIO	41GPIO	29GPIO	21GPIO	17GPIO	20GPIO
	封装C：QFN52 WiFi摄像头 SIP PSRAM						DVP接口	RGB接口	双QSPI接口	单QSPI接口	仅音频接口	仅通用接口
	封装D：QFN40 家电语音WiFi模块 SIP Flash						SIP PSRAM	SIP PSRAM	SIP PSRAM	SIP PSRAM	SIP Flash	-
	封装F：QFN40 通道芯片											
GPIOA_00	●	●	●	●	●	**cp_jtag_tck**	**cp_jtag_tck**	**cp_jtag_tck**	**cp_jtag_tck**	**cp_jtag_tck**	**cp_jtag_tck**	**cp_jtag_tck**
GPIOA_01	●	●	●	●	●	**cp_jtag_tms**	**cp_jtag_tms**	**cp_jtag_tms**	**cp_jtag_tms**	**cp_jtag_tms**	**cp_jtag_tms**	**cp_jtag_tms**
GPIOA_02	●	●	●	●	●	**UART1_TX**	**UART1_TX**	**UART1_TX**	**UART1_TX**	**UART1_TX**	**UART1_TX**	**UART1_TX**
GPIOA_03	●	●	●	●	●	**UART1_RX, Pull Down for Downloa**	**UART1_RX**	**UART1_RX**	**UART1_RX**	**UART1_RX**	**UART1_RX**	**UART1_RX**
GPIOA_04	●	●	●	●	●	**SDIO_DAT2**	LCD_SPI_CLK	HSYNC	LCD_SPI_CLK	CAM_SPI_CLK	uart0_tx	SDIO_DAT2
GPIOA_05	●	●	●	●	●	**SDIO_DAT3**	LCD_SPI_CS	VSYNC	LCD_SPI_CS	CAM_SPI_CS	uart0_rx	SDIO_DAT3
GPIOA_06	●	●	●	●	●	**SDIO_CMD**	LCD_SPI_MISO	DCLK	LCD_SPI_MISO	CAM_SPI_MISO	spi0_clk	SDIO_CMD
GPIOA_07	●	●	●	●	●	**SDIO_CLK**	LCD_SPI_MOSI	DE	LCD_SPI_MOSI	CAM_SPI_MOSI	spi0_cs_n	SDIO_CLK
GPIOA_08	●	●	●	●	●	**SDIO_DAT0**	LCD_SPI_HOLD	DR[2]	LCD_SPI_HOLD	CAM_SPI_HOLD	spi0_miso	SDIO_DAT0
GPIOA_09	●	●	●	●	●	**SDIO_DAT1**	LCD_SPI_WP	DR[3]	LCD_SPI_WP	CAM_SPI_WP	spi0_mosi	SDIO_DAT1
GPIOA_10	●	●					DVP_HSYNC	DR[4]	I2C_SDA			
GPIOA_11	●	●					DVP_VSYNC	DR[5]	I2C_SCL			
GPIOA_12	●						DVP_CLK	DR[6]				
GPIOA_13	●						DVP_DAT4	DR[7]				
GPIOA_14	●						DVP_DAT5	DG[2]				
GPIOA_15	●						DVP_DAT6	DG[3]				
GPIOA_16	●						DVP_DAT7	DG[4]				
GPIOA_17	●						DVP_DAT8	DG[5]				
GPIOA_18	●						DVP_DAT9	DG[6]				
GPIOA_19	●	●					FLASH_WP	DG[7]	CAM_SPI_MISO			
GPIOA_20	●	●			●		FLASH_SO	DB[2]	CAM_SPI_HOLD			PCM_CLK
GPIOA_21	●	●			●		FLASH_CS	DB[3]	CAM_SPI_WP			PCM_SYNC
GPIOA_22	●	●			●		FLASH_SI	DB[4]	CAM_SPI_CLK			PCM_IN
GPIOA_23	●	●			●		FLASH_SCLK	DB[5]	CAM_SPI_CS			PCM_OUT
GPIOA_24	●	●					FLASH_HOLD	DB[6]	CAM_SPI_MISO			
GPIOA_25	●						LCD_RST	DB[7]				
GPIOA_26	●						DVP_RST					
GPIOA_27	●											
GPIOA_28(MIC1_INP)	●	●	●				MIC1_INP	MIC1_INP	MIC1_INP	MIC1_INP		
GPIOA_29(MIC1_INN)	●	●	●				MIC1_INN	MIC1_INN	MIC1_INN	MIC1_INN		
GPIOA_30(MIC0_INP)	●	●	●	●			MIC0_INP	MIC0_INP	MIC0_INP	MIC0_INP	MIC0_INP	
GPIOA_31(MIC0_INN)	●	●	●	●			MIC0_INN	MIC0_INN	MIC0_INN	MIC0_INN	MIC0_INN	
GPIOB_00	●						I2C_SDA	I2C_SDA				
GPIOB_01	●						I2C_SCL	I2C_SCL				
GPIOB_02	●	●	●	●	●		KEYSENSE0	KEYSENSE0	KEYSENSE0	KEYSENSE0	KEYSENSE0	uart0_tx
GPIOB_03	●	●	●	●	●		CAM_LED	LED0	CAM_LED	CAM_LED	KEYSENSE1	uart0_rx
GPIOB_04	●	●	●	●	●		AUDPA_EN	AUDPA_EN	AUDPA_EN	GPIOB_04	I2C_SDA	uart0_cts
GPIOB_05	●	●	●	●	●		PWR_LOCK	PWR_LOCK	PWR_LOCK	I2C_SDA	I2C_SCL	uart0_rts
GPIOB_06	●	●	●	●	●		DVP_DAT10	TP_INT	TP_INT	I2C_SCL	AUDPA_EN	bt_wakeup
GPIOB_07	●	●	●		●		DVP_DAT11	TP_RST	TP_RST	GPIOB_07		wifi_wakeup
GPIOB_08	●	●	●				TP_INT	LCD_RST	LCD_RST	GPIOB_08		
GPIOB_09	●						TP_RST					
FLASH_WP	●	●	●			**WP**	SD_DAT1	FLASH_WP	SD_DAT1	FLASH_WP		
FLASH_SO	●	●	●			**SO**	SD_DAT0	FLASH_SO	SD_DAT0	FLASH_SO		
FLASH_CS	●	●	●			**CS**	SD_CLK	FLASH_CS	SD_CLK	FLASH_CS		
FLASH_SI	●	●	●			**SI**	SD_CMD	FLASH_SI	SD_CMD	FLASH_SI		
FLASH_SCLK	●	●	●			**SCLK**	SD_DAT3	FLASH_SCLK	SD_DAT3	FLASH_SCLK		
FLASH_HOLD	●	●	●			**HOLD**	SD_DAT2	FLASH_HOLD	SD_DAT2	FLASH_HOLD		
VBAT	●	●	●	●	●							
AVDD18_AUD												
AVDD_AUD_OUT	●	●	●	●								
VREF_DAC												
VREF_ADC	●	●	●	●								
AVDD_AUD_IN												
AVDD_MEM_IN	●	●	●	●	●							
AVDD33_PA_OUT	●	●	●	●	●							
AVDD12_DIG												
AVDD12_ADDA	●	●	●	●	●							
AVDD12_RX	●	●	●	●	●							
RF_INOUT	●	●	●	●	●							
AVDD_PA1												
AVDD_PA2	●	●	●	●	●							
AVDD_PPA	●	●	●	●	●							
AVDD12_PLL												
AVDD12_VCO	●	●	●	●	●							
USB_VCC												
VDD_IO_AON	●	●	●		●							
VDD_AON	●	●	●	●	●							
VDD_IO2	●	●	●	●	●							
VDD_CORE	● ● ● ●	● ● ●	● ● ●	● ● ●	● ● ●							
VDD_IO	●	●	●	●	●							
MIC_BIAS	●	●	●	●								
LIN_OUTP	●	●	●	●								
LIN_OUTN	●	●	●	●								
XO24M_OUT	●	●	●	●	●							
XO24M_IN	●	●	●	●	●							
RSTN	●	●	●	●	●							
USB_DP	●	●	●		●							
USB_DM	●	●	●		●							

PAD Name	1.0模组			备注	Function Mode																													
	简称	说明			Function 0	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6	Function 7	Function 8	Function 9	Function 10	Function 11	Function 12	Function 13	Function 14	Function 15	Function 16	Function 17	Function 18	Function 19	Function 20	Function 21	Function 22	Function 23	Function 24	Function 25	Function 26	Function 27	Function 28	Function 29	Function 30	Function 31
GPIOA_00	LCD_PWM	JTAG_TCK		LCM模组	jtag_tck	gpio0_00	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bclk	i2s1_bclk	gpt_pause	gpt_pwm_0	pr_datout	srmii_clk		vic_pixel_data6	sdwc_e_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug0	aud_dmic_clk				wf_la_data0	snc_spi0_ss_n	snc_spi3_wp_n	
GPIOA_01	LCD RST	JTAG_TMS			jtag_tms	gpio0_01	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_clk_11	gpt_pwm_1	pr_datin	srmii_cmd		vic_pixel_data9	sdwc_e_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_ffoctrl_start	modem_debug1	aud_dmic0_in				wf_la_data1	snc_spi0_miso	snc_spi3_cs_n	
GPIOA_02	UART0 RX		LOAD&打印SCP日志		gpio0_02	flashio_cs1_n	uart0_rxd	uart1_txd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	pr_datout	srmii_da00	sdioh_dat3	vic_pixel_data10	sdwc_y_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug2	aud_dmic_clk	usbphy_scan_clk	usb_ponrst	rtfbd_dbg_port_0	wf_la_data18	snc_spi0_mosi	snc_spi3_mosi	
GPIOA_03	UART0 TX				gpio0_03	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	pr_datin	srmii_da01	sdioh_da02	vic_pixel_data11	sdwc_y_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_ffoctrl_start	modem_debug3	aud_dmic_clk	usbphy_scan_rst	usb_vcontrol0	rtfbd_dbg_port_1	wf_la_data19	snc_spi0_clk	snc_spi3_miso	
GPIOA_04	SD DAT1				gpio0_04	jtag_tck	uart0_rxd	uart1_txd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bclk	i2s1_bclk	gpt_trigger_2	gpt_pwm_4	pr_datout	srmii_da02	sdioh_dat1	vic_pixel_data0	sdwc_z_a	dbg_clk	dbg_out_4	dbg_in_4	guard	bt_trigger	modem_debug4	aud_dmic0_in	usbphy_scan_se	usb_vcontrol1	rtfbd_dbg_port_2	wf_la_data0	snc_spi0_clk	snc_spi3_clk	
GPIOA_05	SD DATO				gpio0_05	jtag_tms	uart0_txd	uart1_rxd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	pr_datin	srmii_da03	sdioh_da00	vic_pixel_data1	sdwc_z_b	dbg_clk	dbg_out_5	dbg_in_5	guard	bt_ffoctrl_start	modem_debug5	aud_dmic0_in	usbphy_scan_si1	usb_vcontrol2	rtfbd_dbg_port_3	wf_la_data1	snc_spi0_cs_n	snc_spi3_cs_n	hsync
GPIOA_06	SD CLK		TF CARD		gpio0_06	jtag_tdi	uart0_rxd	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	pr_datout	srmii_cmd	sdioh_clk	vic_pixel_data2	sdwc_x_a	dbg_clk	dbg_out_6	dbg_in_6	guard	bt_trigger	modem_debug6	aud_dmic_clk	usbphy_scan_si2	usb_vcontrol3	rtfbd_dbg_port_4	wf_la_data2	snc_spi0_miso	snc_spi3_miso	i_lk_rgb_out
GPIOA_07	SD CMD				gpio0_07	jtag_tdo	uart0_txd	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	pr_datin	srmii_clk	sdioh_cmd	vic_pixel_data3	sdwc_x_b	dbg_clk	dbg_out_7	dbg_in_7	guard	bt_ffoctrl_start	modem_debug7	aud_dmic0_in	usbphy_scan_si3	usb_corecilkin	rtfbd_dbg_port_5	wf_la_data3	snc_spi0_mosi	snc_spi3_mosi	hs
GPIOA_08	SD DAT2				jtag_tdi	gpio0_08	uart0_txd	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bclk	i2s1_bclk	gpt_trigger_6	gpt_pwm_0	pr_datout	srmii_da00	sdioh_dat3	vic_h_sync	sdwc_y_a	dbg_clk	dbg_out_8	dbg_in_8	guard	bt_trigger	modem_debug8	aud_dmic_clk	usbphy_scan_si4	usb_utmi_linestate0	rtfbd_dbg_port_6	wf_la_data4	snc_spi0_hold_n	snc_spi3_hold_n	tr2
GPIOA_09	SD DAT3				jtag_tdo	gpio0_09	uart0_rxd	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_1	pr_datin	srmii_da01	sdioh_dat2	vic_v_sync	sdwc_y_b	dbg_clk	dbg_out_9	dbg_in_9	guard	bt_ffoctrl_start	modem_debug9	aud_dmic0_in	usbphy_scan_so1	usb_utmi_linestate1	rtfbd_dbg_port_7	wf_la_data5	snc_spi0_wp_n	snc_spi3_wp_n	tr3
GPIOA_10	vic_h_sync				gpio0_10	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	pr_datout	srmii_da02	sdioh_cd	vic_h_sync	sdwc_z_a	dbg_clk	dbg_out_10	dbg_in_10	guard	bt_trigger	modem_debug10	aud_dmic_clk	usbphy_scan_so2		rtfbd_dbg_port_8	wf_la_data6	snc_spi0_clk	snc_spi3_cs_n	tr4
GPIOA_11	vic_v_sync				gpio0_11	flashio_cs1_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	pr_datin	srmii_da03	sdioh_wp	vic_v_sync	sdwc_z_b	dbg_clk	dbg_out_11	dbg_in_11	guard	bt_ffoctrl_start	modem_debug7	aud_dmic0_in			rtfbd_dbg_port_9	wf_la_data7	snc_spi0_cs_n	snc_spi3_clk	tr5
GPIOA_12	vic_pixel_clk				jtag_tck	uart0_cts_n	uart1_rxd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bclk	i2s1_bclk	gpt_trigger_2	gpt_pwm_4	pr_datout	srmii_cmd	sdioh_cd	vic_pixel_clk	sdwc_a_a	dbg_clk	dbg_out_12	dbg_in_12	guard	bt_trigger	modem_debug8	aud_dmic_clk	usbphy_scan_so3		rtfbd_dbg_port_10	wf_la_data8	snc_spi0_miso	snc_spi3_wp_n	tr6
GPIOA_13	vic_pixel_data4				jtag_tms	uart0_rxd	uart1_txd	uart1_cts_n	uart2_cts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	pr_datin	srmii_clk	sdioh_wp	vic_pixel_data4	sdwc_a_b	dbg_clk	dbg_out_13	dbg_in_13	guard	bt_ffoctrl_start	modem_debug9	aud_dmic0_in	usbphy_scan_so4		rtfbd_dbg_port_11	wf_la_data9	snc_spi0_mosi	snc_spi3_cs_n	tr7
GPIOA_14	vic_pixel_data5			兼容SPI摄像头	jtag_tdi	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	pr_datout	srmii_da00		vic_pixel_data5	sdwc_y_a	dbg_clk	dbg_out_14	dbg_in_14	guard	bt_trigger	modem_debug10	aud_dmic_clk			rtfbd_dbg_port_12	wf_la_data10	snc_spi0_hold_n	snc_spi3_miso	tg2
GPIOA_15	vic_pixel_data6				jtag_tdo	uart0_rts_n	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	pr_datin	srmii_da01		vic_pixel_data6	sdwc_y_b	dbg_clk	dbg_out_15	dbg_in_15	guard	bt_ffoctrl_start	modem_debug11	aud_dmic0_in			rtfbd_dbg_port_13	wf_la_data11	snc_spi0_wp_n	snc_spi3_mosi	tg3
GPIOA_16	vic_pixel_data7				jtag_tck	uart0_cts_n	uart1_rxd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bclk	i2s1_bclk	gpt_trigger_6	gpt_pwm_0	pr_datout	srmii_da02		vic_pixel_data7	sdwc_z_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug12	aud_dmic_clk			rtfbd_dbg_port_14	wf_la_data12	snc_spi0_clk	snc_spi3_hold_n	tg4
GPIOA_17	vic_pixel_data8				jtag_tms	uart0_rts_n	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_clk_11	pr_datin	srmii_da03		vic_pixel_data8	sdwc_z_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_ffoctrl_start	modem_debug13	aud_dmic0_in			rtfbd_dbg_port_15	wf_la_data13	snc_spi0_cs_n	snc_spi3_clk	tg5
GPIOA_18	vic_pixel_data9				jtag_tdi	flashio_cs1_n	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_11	gpt_pause	pr_datout	srmii_cmd		vic_pixel_data9	sdwc_x_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug14	aud_dmic_clk			rtfbd_dbg_port_16	wf_la_data14	snc_spi0_miso	snc_spi3_wp_n	tg6
GPIOA_19	vic_pixel_data10				gpio0_19	flashio_wp_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_11	pr_datin	srmii_clk		vic_pixel_data10	sdwc_x_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_ffoctrl_start	modem_debug15	aud_dmic0_in			rtfbd_dbg_port_17	wf_la_data15	snc_spi0_mosi	snc_spi3_cs_n	tg7
GPIOA_20	vic_pixel_data11				gpio0_20	flashio_miso	uart0_cts_n	uart1_rxd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bclk	i2s1_bclk	gpt_trigger_0	gpt_pwm_0	pr_datout	srmii_da00		vic_pixel_data11	sdwc_y_a	dbg_clk	dbg_out_4	dbg_in_4	guard	bt_trigger	modem_debug16	aud_dmic_clk			rtfbd_dbg_port_18	wf_la_data16	snc_spi0_hold_n	snc_spi3_miso	tb2
GPIOA_21	UART1_TX	JTAG调试日志			gpio0_21	flashio_cs0_n	uart0_rxd	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_1	gpt_pwm_1	pr_datin	srmii_da01		vic_pixel_data0	sdwc_y_b	dbg_clk	dbg_out_5	dbg_in_5	guard	bt_ffoctrl_start	modem_debug17	aud_dmic0_in			rtfbd_dbg_port_19	wf_la_data17	snc_spi0_wp_n	snc_spi3_cs_n	tb3
GPIOA_22	I2C_SDA		双线I2C打印复用		gpio0_22	flashio_mosi				spi0_cs_n	spi1_cs_n	spi2_cs_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat						vic_pixel_data1					guard							wf_la_data18	snc_spi0_clk	snc_spi3_clk	tb4
GPIOA_23	I2C0 SCL			LCM模组	gpio0_23	flashio_clk				spi0_miso	spi1_miso	spi2_miso	i2c0_scl	i2s0_dacdat	i2s1_dacdat						vic_pixel_data2					guard							wf_la_data19	snc_spi0_cs_n	snc_spi3_miso	tb5
GPIOA_24	TP RST	触摸中断	触摸中断		gpio0_24	flashio_hold_n				spi0_mosi	spi1_mosi	spi2_wp_n	i2c1_scl	i2s0_bclk	i2s1_bclk						vic_pixel_data3					guard								snc_spi0_miso	snc_spi3_miso	tb6
GPIOA_25	TP RST	触摸复位																															snc_spi0_mosi	snc_spi3_wp_n	tb7
GPIOA_26	MIC_CLK_OUT	MCLK								spi0_wp_n	spi1_wp_n	spi2_mosi	i2c0_scl	i2s0_adcdat	i2s1_adcdat						vic_clk_out												sena_gpioa_26	snc_spi0_hold_n	snc_spi3_hold_n	
GPIOA_27	PA EN	功放MUTE			gpio0_27					spi0_hold_n	spi1_hold_n	spi2_hold_n	i2c0_sda	i2s0_dacdat	i2s1_dacdat																		sena_gpioa_27			
GPIOA_28	MIC1 INP				gpio0_28	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_sda	i2s0_bclk	i2s1_bclk	gpt_clk_11	gpt_pause	pr_datout	srmii_da02		vic_pixel_data0	sdwc_z_a	dbg_clk	dbg_out_6	dbg_in_6	guard	bt_trigger	modem_debug18	aud_dmic_clk			rtfbd_dbg_port_20	wf_la_data20	snc_spi0_wp_n	snc_spi1_hold_n	
GPIOA_29	MIC1 INN	地麦			gpio0_29	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_clk_11	pr_datin	srmii_da03		vic_pixel_data1	sdwc_z_b	dbg_clk	dbg_out_7	dbg_in_7	guard	bt_ffoctrl_start	modem_debug19	aud_dmic0_in			rtfbd_dbg_port_21	wf_la_data21	snc_spi0_hold_n	snc_spi3_clk	
GPIOA_30	MIC2 INP				gpio0_30	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_0	pr_datout	srmii_clk		vic_pixel_data2	sdwc_x_a	dbg_clk	dbg_out_8	dbg_in_8	guard	bt_trigger	modem_debug20	aud_dmic_clk			rtfbd_dbg_port_22	wf_la_data22	snc_spi0_cs_n	snc_spi3_wp_n	
GPIOA_31	MIC2 INN	地麦			gpio0_31	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_1	pr_datin	srmii_cmd		vic_pixel_data3	sdwc_x_b	dbg_clk	dbg_out_9	dbg_in_9	guard	bt_ffoctrl_start	modem_debug21	aud_dmic0_in			rtfbd_dbg_port_23	wf_la_data23	snc_spi0_miso	snc_spi3_cs_n	
GPIOB_00	LCD SPI MISO	D1		LCM模组	gpio1_00	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bclk	i2s1_bclk	gpt_trigger_2	gpt_pwm_2	pr_datout	srmii_rstn_in	sdioh_io_rstn	vic_h_sync	sdwc_y_a	dbg_clk	dbg_out_10	dbg_in_10	guard	bt_trigger	modem_debug22	aud_dmic_clk			rtfbd_dbg_port_24	wf_la_data18	snc_spi0_mosi	snc_spi1_miso	gr0
GPIOB_01	LCD SPI MOSI	D0			gpio1_01	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_3	pr_datin	srmii_rstn_in	sdioh_io_rstn	vic_pixel_clk	sdwc_y_b	dbg_clk	dbg_out_11	dbg_in_11	guard	bt_ffoctrl_start	modem_debug23	aud_dmic0_in			rtfbd_dbg_port_25	wf_la_data19	snc_spi0_clk	snc_spi3_mosi	tf1
GPIOB_02	LCD SPI HOLD	D3			gpio1_02	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_4	pr_datout	srmii_da02		vic_h_sync	sdwc_z_a	dbg_clk	dbg_out_12	dbg_in_12	guard	bt_trigger	modem_debug24	aud_dmic_clk			rtfbd_dbg_port_26	wf_la_data2	snc_spi0_wp_n	snc_spi3_hold_n	tg0
GPIOB_03	LCD SPI CLK	CLK			gpio1_03	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_5	pr_datin	srmii_da03		vic_v_sync	sdwc_z_b	dbg_clk	dbg_out_13	dbg_in_13	guard	bt_ffoctrl_start	modem_debug25	aud_dmic0_in			rtfbd_dbg_port_27	wf_la_data3	snc_spi0_hold_n	snc_spi3_clk	tg1
GPIOB_04	LCD SPI WP	D2			gpio1_04	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_bclk	i2s1_bclk	gpt_trigger_6	gpt_pwm_6	pr_datout	srmii_da04		vic_clk_out	sdwc_x_a	dbg_clk	dbg_out_14	dbg_in_14	guard	bt_trigger	modem_debug0	aud_dmic_clk			rtfbd_dbg_port_28	wf_la_data4	snc_spi0_cs_n	snc_spi1_wp_n	tb0
GPIOB_05	LCD SPI CS	CS			gpio1_05	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_7	pr_datin	srmii_da05		vic_pixel_data0	sdwc_x_b	dbg_clk	dbg_out_15	dbg_in_15	guard	bt_ffoctrl_start	modem_debug1	aud_dmic0_in			rtfbd_dbg_port_29	wf_la_data5	snc_spi0_miso	snc_spi1_cs_n	tb1
GPIOB_06	KEY1	ADC按键			gpio1_06	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_11	gpt_pause	pr_datout	srmii_da06		vic_pixel_data10	sdwc_y_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug2	aud_dmic_clk		usb_bist_ok	rtfbd_dbg_port_30	wf_la_data6	snc_spi0_mosi	snc_spi1_clk	
GPIOB_07	KEY2	触摸按键			gpio1_07	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_11	pr_datin	srmii_da07		vic_pixel_data11	sdwc_y_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_ffoctrl_start	modem_debug3	aud_dmic0_in			rtfbd_dbg_port_31	wf_la_data7	snc_spi0_clk	snc_spi1_miso	
GPIOB_08	LCD_TE	LCD TE同步			gpio1_08	flashio_cs1_n	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bclk	i2s1_bclk	gpt_clk_11	gpt_pwm_0	pr_datout	srmii_clk		vic_v_sync	sdwc_y_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug4	aud_dmic_clk				wf_la_data0				
GPIOB_09	LED	硬件指示灯		供	gpio1_09	flashio_cs1_n	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_pwm_1	pr_datin	srmii_cmd		vic_pixel_clk	sdwc_y_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_ffoctrl_start	modem_debug5	aud_dmic0_in				wf_la_data1				

FLASH_CS_N	FLASH_CS_N	FLASH_CS_N			flashio_cs0_n	sdioh_clk																														
FLASH_MISO	FLASH_MISO	FLASH_MISO			flashio_miso	sdioh_dat0																														
FLASH_WP_N	FLASH_WP_N	FLASH_WP_N	Boot		flashio_wp_n	sdioh_dat1	FLASH WP																													
FLASH_HOLD_N	FLASH_HOLD_N	FLASH_HOLD_N	Flash		flashio_hold_n	sdioh_dat2																														
FLASH_CLK	FLASH_CLK	FLASH_CLK			flashio_clk	sdioh_dat3																														
FLASH_MOSI	FLASH_MOSI	FLASH_MOSI			flashio_mosi	sdioh_cmd																														

USB_DP			USB口																																
USB_DM																																		
LIN_OUTP			LIN_OUTP																															
LIN_OUTN			LIN_OUTN																															

aon_iomux							
PAD Name	Function Mode						
	Function 0	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6
GPIOB_00	aon_dbg_out_0	aon_gpio_wkup_0	aon_gpio_out_0		bt_wakeup_ext	normal_gpiob_00	xtal_32k_in
GPIOB_01	aon_dbg_out_1	aon_gpio_wkup_1	aon_gpio_out_1		bt_wakeup_ext	normal_gpiob_01	xtal_32k_in
GPIOB_02	aon_dbg_out_2	aon_gpio_wkup_2	aon_gpio_out_2	ana_gpiob_02	bt_wakeup_ext	normal_gpiob_02	xtal_32k_in
GPIOB_03	aon_dbg_out_3	aon_gpio_wkup_3	aon_gpio_out_3	ana_gpiob_03	bt_wakeup_ext	normal_gpiob_03	xtal_32k_in
GPIOB_04	aon_dbg_out_4	aon_gpio_wkup_4	aon_gpio_out_4	ana_gpiob_04	bt_wakeup_ext	normal_gpiob_04	xtal_32k_in
GPIOB_05	aon_dbg_out_5	aon_gpio_wkup_5	aon_gpio_out_5	ana_gpiob_05	bt_wakeup_ext	normal_gpiob_05	xtal_32k_in
GPIOB_06	aon_dbg_out_6	aon_gpio_wkup_6	aon_gpio_out_6	ana_gpiob_06	bt_wakeup_ext	normal_gpiob_06	xtal_32k_in
GPIOB_07	aon_gpio_wkup_7	aon_dbg_out_7	aon_gpio_out_7	ana_gpiob_07	bt_wakeup_ext	normal_gpiob_07	xtal_32k_in
GPIOB_08	aon_dbg_out_8	aon_gpio_wkup_8	aon_gpio_out_8		bt_wakeup_ext	normal_gpiob_08	xtal_32k_in
GPIOB_09	aon_dbg_out_9	aon_gpio_wkup_9	aon_gpio_out_9		bt_wakeup_ext	normal_gpiob_09	xtal_32k_in

ana_iomux				
PAD Name	AON Function	AON_ANA_SEL(pad_aon_gpiob_<ARRAY_INDEX>_ana_sel)		
		0	1	5
GPIOB_02	Function 3	GPADC_VIN<0>	PAD_CTP<0>	KEYSENSE<0>
GPIOB_03	Function 3	GPADC_VIN<1>	PAD_CTP<1>	KEYSENSE<1>
GPIOB_04	Function 3	GPADC_VIN<2>	PAD_CTP<2>	
GPIOB_05	Function 3	GPADC_VIN<3>	PAD_CTP<3>	
GPIOB_06	Function 3	GPADC_VIN<4>	PAD_CTP<4>	
GPIOB_07	Function 3	GPADC_VIN<5>	PAD_CTP<5>	

core_iomux (1)																
	Function Mode															
PAD Name	Function 0	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6	Function 7	Function 8	Function 9	Function 10	Function 11	Function 12	Function 13	Function 14	Function 15
GPIOA_00	jtag_tck	gpio0_00	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bck	i2s1_bck	gpt_pause	gpt_pwm_0	ir_datout	smid_clk	
GPIOA_01	jtag_tms	gpio0_01	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_clk_t1	gpt_pwm_1	ir_datin	smid_cmd	
GPIOA_02	gpio0_02	flashio_cs1_n	uart0_rxd	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	ir_datout	smid_dat0	sdioh_dat3
GPIOA_03	gpio0_03	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	ir_datin	smid_dat1	sdioh_dat2
GPIOA_04	gpio0_04	jtag_tck	uart0_rxd	uart1_txd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	ir_datout	smid_dat2	sdioh_dat1
GPIOA_05	gpio0_05	jtag_tms	uart0_txd	uart1_rxd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	ir_datin	smid_dat3	sdioh_dat0
GPIOA_06	gpio0_06	jtag_tdi	uart0_rxd	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	ir_datout	smid_cmd	sdioh_clk
GPIOA_07	gpio0_07	jtag_tdo	uart0_rxd	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	ir_datin	smid_clk	sdioh_cmd
GPIOA_08	jtag_tdi	gpio0_08	uart0_txd	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pwm_0	ir_datout	smid_dat0	sdioh_dat3
GPIOA_09	jtag_tdo	gpio0_09	uart0_rxd	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_1	ir_datin	smid_dat1	sdioh_dat2
GPIOA_10	gpio0_10	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	ir_datout	smid_dat2	sdioh_cd
GPIOA_11	gpio0_11	flashio_cs1_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	ir_datin	smid_dat3	sdioh_wp
GPIOA_12	gpio0_12	jtag_tck	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	ir_datout	smid_cmd	sdioh_cd
GPIOA_13	gpio0_13	jtag_tms	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	ir_datin	smid_clk	sdioh_wp
GPIOA_14	gpio0_14	jtag_tdi	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	ir_datout	smid_dat0	
GPIOA_15	gpio0_15	jtag_tdo	uart0_rts_n	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	ir_datin	smid_dat1	
GPIOA_16	gpio0_16	flashio_cs1_n	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pause	ir_datout	smid_dat2	
GPIOA_17	gpio0_17	flashio_cs1_n	uart0_rxd	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_clk_t1	ir_datin	smid_dat3	
GPIOA_18	gpio0_18	flashio_cs1_n	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_t1	gpt_pause	ir_datout	smid_cmd	
GPIOA_19	gpio0_19	flashio_wp_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_t1	ir_datin	smid_clk	
GPIOA_20	gpio0_20	flashio_miso	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_0	gpt_pwm_0	ir_datout	smid_dat0	
GPIOA_21	gpio0_21	flashio_cs0_n	uart0_rxd	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_1	gpt_pwm_1	ir_datin	smid_dat1	
GPIOA_22	gpio0_22	flashio_mosi				spi0_cs_n	spi1_cs_n	spi2_cs_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat					
GPIOA_23	gpio0_23	flashio_clk				spi0_miso	spi1_miso	spi2_miso	i2c0_scl	i2s0_dacdat	i2s1_dacdat					
GPIOA_24	gpio0_24	flashio_hold_n				spi0_mosi	spi1_mosi	spi2_wp_n	i2c1_scl	i2s0_bck	i2s1_bck					
GPIOA_25	gpio0_25					spi0_clk	spi1_clk	spi2_clk	i2c1_sda	i2s0_lrck	i2s1_lrck					
GPIOA_26	gpio0_26					spi0_wp_n	spi1_wp_n	spi2_mosi	i2c0_scl	i2s0_adcdat	i2s1_adcdat					
GPIOA_27	gpio0_27					spi0_hold_n	spi1_hold_n	spi2_hold_n	i2c0_sda	i2s0_dacdat	i2s1_dacdat					
GPIOA_28	gpio0_28	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_sda	i2s0_bck	i2s1_bck	gpt_clk_t1	gpt_pause	ir_datout	smid_dat2	
GPIOA_29	gpio0_29	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_clk_t1	ir_datin	smid_dat3	
GPIOA_30	gpio0_30	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_0	ir_datout	smid_clk	
GPIOA_31	gpio0_31	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_1	ir_datin	smid_cmd	
GPIOB_00	gpio1_00	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_2	ir_datout	smid_rstn_in	sdioh_io_rstn
GPIOB_01	gpio1_01	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_3	ir_datin	smid_rstn_in	sdioh_io_rstn
GPIOB_02	gpio1_02	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_4	ir_datout	smid_dat2	
GPIOB_03	gpio1_03	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_5	ir_datin	smid_dat3	
GPIOB_04	gpio1_04	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pwm_6	ir_datout	smid_dat4	
GPIOB_05	gpio1_05	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_7	ir_datin	smid_dat5	
GPIOB_06	gpio1_06	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_t1	gpt_pause	ir_datout	smid_dat6	
GPIOB_07	gpio1_07	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_t1	ir_datin	smid_dat7	
GPIOB_08	gpio1_08	flashio_cs1_n	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bck	i2s1_bck	gpt_clk_t1	gpt_pwm_0	ir_datout	smid_clk	
GPIOB_09	gpio1_09	flashio_cs1_n	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_pwm_1	ir_datin	smid_cmd	
FLASH_CS_N	flashio_cs0_n	sdioh_clk														
FLASH_MISO	flashio_miso	sdioh_dat0														
FLASH_WP_N	flashio_wp_n	sdioh_dat1														
FLASH_HOLD_N	flashio_hold_n	sdioh_dat2														
FLASH_CLK	flashio_clk	sdioh_dat3														
FLASH_MOSI	flashio_mosi	sdioh_cmd														

core_iomux (2)																
PAD Name	Function Mode															
	Function 16	Function 17	Function 18	Function 19	Function 20	Function 21	Function 22	Function 23	Function 24	Function 25	Function 26	Function 27	Function 28	Function 29	Function 30	Function 31
GPIOA_00	vic_pixel_data8	qdec_x_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug0	aud_dmic_clk				wf_la_data0	vic_spi0_cs_n	voc_spi1_wp_n	
GPIOA_01	vic_pixel_data9	qdec_x_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_fifoctrl_start	modem_debug1	aud_dmic0_in				wf_la_data1	vic_spi0_miso	voc_spi1_cs_n	
GPIOA_02	vic_pixel_data10	qdec_y_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug0	aud_dmic_clk	usbphy_scan_clk	usb_ponrst	rfbd_dbg_port_0	wf_la_data18	vic_spi0_mosi	voc_spi1_miso	
GPIOA_03	vic_pixel_data11	qdec_y_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_fifoctrl_start	modem_debug1	aud_dmic_clk	usbphy_scan_rst	usb_vcontrol0	rfbd_dbg_port_1	wf_la_data19	vic_spi0_clk	voc_spi1_mosi	
GPIOA_04	vic_pixel_data0	qdec_z_a	dbg_clk	dbg_out_4	dbg_in_4	guard	bt_trigger	modem_debug2	aud_dmic0_in	usbphy_scan_se	usb_vcontrol1	rfbd_dbg_port_2	wf_la_data0	vic_spi0_clk	voc_spi1_clk	hsync
GPIOA_05	vic_pixel_data1	qdec_z_b	dbg_clk	dbg_out_5	dbg_in_5	guard	bt_fifoctrl_start	modem_debug3	aud_dmic0_in	usbphy_scan_si1	usb_vcontrol2	rfbd_dbg_port_3	wf_la_data1	vic_spi0_cs_n	voc_spi1_cs_n	vsync
GPIOA_06	vic_pixel_data2	qdec_x_a	dbg_clk	dbg_out_6	dbg_in_6	guard	bt_trigger	modem_debug2	aud_dmic_clk	usbphy_scan_si2	usb_vcontrol3	rfbd_dbg_port_4	wf_la_data2	vic_spi0_miso	voc_spi1_miso	clk_rgb_out
GPIOA_07	vic_pixel_data3	qdec_x_b	dbg_clk	dbg_out_7	dbg_in_7	guard	bt_fifoctrl_start	modem_debug3	aud_dmic0_in	usbphy_scan_si3	usb_coreclkin	rfbd_dbg_port_5	wf_la_data3	vic_spi0_mosi	voc_spi1_mosi	de
GPIOA_08	vic_h_sync	qdec_y_a	dbg_clk	dbg_out_8	dbg_in_8	guard	bt_trigger	modem_debug4	aud_dmic_clk	usbphy_scan_si4	usb_utmi_linestate0	rfbd_dbg_port_6	wf_la_data4	vic_spi0_hold_n	voc_spi1_hold_n	dr2
GPIOA_09	vic_v_sync	qdec_y_b	dbg_clk	dbg_out_9	dbg_in_9	guard	bt_fifoctrl_start	modem_debug5	aud_dmic0_in	usbphy_scan_so1	usb_utmi_linestate1	rfbd_dbg_port_7	wf_la_data5	vic_spi0_wp_n	voc_spi1_wp_n	dr3
GPIOA_10	vic_h_sync	qdec_z_a	dbg_clk	dbg_out_10	dbg_in_10	guard	bt_trigger	modem_debug6	aud_dmic_clk	usbphy_scan_so2		rfbd_dbg_port_8	wf_la_data6	vic_spi0_clk	voc_spi1_cs_n	dr4
GPIOA_11	vic_v_sync	qdec_z_b	dbg_clk	dbg_out_11	dbg_in_11	guard	bt_fifoctrl_start	modem_debug7	aud_dmic0_in			rfbd_dbg_port_9	wf_la_data7	vic_spi0_cs_n	voc_spi1_clk	dr5
GPIOA_12	vic_pixel_clk	qdec_x_a	dbg_clk	dbg_out_12	dbg_in_12	guard	bt_trigger	modem_debug8	aud_dmic_clk	usbphy_scan_so3		rfbd_dbg_port_10	wf_la_data8	vic_spi0_miso	voc_spi1_wp_n	dr6
GPIOA_13	vic_pixel_data4	qdec_x_b	dbg_clk	dbg_out_13	dbg_in_13	guard	bt_fifoctrl_start	modem_debug9	aud_dmic0_in	usbphy_scan_so4		rfbd_dbg_port_11	wf_la_data9	vic_spi0_mosi	voc_spi1_cs_n	dr7
GPIOA_14	vic_pixel_data5	qdec_y_a	dbg_clk	dbg_out_14	dbg_in_14	guard	bt_trigger	modem_debug10	aud_dmic_clk			rfbd_dbg_port_12	wf_la_data10	vic_spi0_hold_n	voc_spi1_miso	dg2
GPIOA_15	vic_pixel_data6	qdec_y_b	dbg_clk	dbg_out_15	dbg_in_15	guard	bt_fifoctrl_start	modem_debug11	aud_dmic0_in			rfbd_dbg_port_13	wf_la_data11	vic_spi0_wp_n	voc_spi1_mosi	dg3
GPIOA_16	vic_pixel_data7	qdec_z_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug12	aud_dmic_clk			rfbd_dbg_port_14	wf_la_data12	vic_spi0_clk	voc_spi1_hold_n	dg4
GPIOA_17	vic_pixel_data8	qdec_z_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_fifoctrl_start	modem_debug13	aud_dmic0_in			rfbd_dbg_port_15	wf_la_data13	vic_spi0_cs_n	voc_spi1_clk	dg5
GPIOA_18	vic_pixel_data9	qdec_x_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug14	aud_dmic_clk			rfbd_dbg_port_16	wf_la_data14	vic_spi0_miso	voc_spi1_wp_n	dg6
GPIOA_19	vic_pixel_data10	qdec_x_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_fifoctrl_start	modem_debug15	aud_dmic0_in			rfbd_dbg_port_17	wf_la_data15	vic_spi0_mosi	voc_spi1_cs_n	dg7
GPIOA_20	vic_pixel_data11	qdec_y_a	dbg_clk	dbg_out_4	dbg_in_4	guard	bt_trigger	modem_debug16	aud_dmic_clk			rfbd_dbg_port_18	wf_la_data16	vic_spi0_hold_n	voc_spi1_miso	db2
GPIOA_21	vic_pixel_data0	qdec_y_b	dbg_clk	dbg_out_5	dbg_in_5	guard	bt_fifoctrl_start	modem_debug17	aud_dmic0_in			rfbd_dbg_port_19	wf_la_data17	vic_spi0_wp_n	voc_spi1_cs_n	db3
GPIOA_22	vic_pixel_data1					guard							wf_la_data18	vic_spi0_clk	voc_spi1_clk	db4
GPIOA_23	vic_pixel_data2					guard							wf_la_data19	vic_spi0_cs_n	voc_spi1_mosi	db5
GPIOA_24	vic_pixel_data3					guard								vic_spi0_miso	voc_spi1_miso	db6
GPIOA_25	vic_clk_out					guard								vic_spi0_mosi	voc_spi1_wp_n	db7
GPIOA_26	vic_clk_out					ana_gpioa_26								vic_spi0_hold_n	voc_spi1_hold_n	
GPIOA_27						ana_gpioa_27										
GPIOA_28	vic_pixel_data0	qdec_z_a	dbg_clk	dbg_out_6	dbg_in_6	guard	bt_trigger	modem_debug18	aud_dmic_clk			rfbd_dbg_port_20	wf_la_data20	vic_spi0_wp_n	voc_spi1_hold_n	
GPIOA_29	vic_pixel_data1	qdec_z_b	dbg_clk	dbg_out_7	dbg_in_7	guard	bt_fifoctrl_start	modem_debug19	aud_dmic0_in			rfbd_dbg_port_21	wf_la_data21	vic_spi0_hold_n	voc_spi1_clk	
GPIOA_30	vic_pixel_data2	qdec_x_a	dbg_clk	dbg_out_8	dbg_in_8	guard	bt_trigger	modem_debug20	aud_dmic_clk			rfbd_dbg_port_22	wf_la_data22	vic_spi0_cs_n	voc_spi1_wp_n	
GPIOA_31	vic_pixel_data3	qdec_x_b	dbg_clk	dbg_out_9	dbg_in_9	guard	bt_fifoctrl_start	modem_debug21	aud_dmic0_in			rfbd_dbg_port_23	wf_la_data23	vic_spi0_miso	voc_spi1_cs_n	
GPIOB_00	vic_h_sync	qdec_y_a	dbg_clk	dbg_out_10	dbg_in_10	guard	bt_trigger	modem_debug22	aud_dmic_clk			rfbd_dbg_port_24	wf_la_data18	vic_spi0_mosi	voc_spi1_miso	dr0
GPIOB_01	vic_pixel_clk	qdec_y_b	dbg_clk	dbg_out_11	dbg_in_11	guard	bt_fifoctrl_start	modem_debug23	aud_dmic0_in			rfbd_dbg_port_25	wf_la_data19	vic_spi0_clk	voc_spi1_mosi	dr1
GPIOB_02	vic_h_sync	qdec_z_a	dbg_clk	dbg_out_12	dbg_in_12	guard	bt_trigger	modem_debug24	aud_dmic_clk			rfbd_dbg_port_26	wf_la_data2	vic_spi0_wp_n	voc_spi1_hold_n	dg0
GPIOB_03	vic_v_sync	qdec_z_b	dbg_clk	dbg_out_13	dbg_in_13	guard	bt_fifoctrl_start	modem_debug25	aud_dmic0_in			rfbd_dbg_port_27	wf_la_data3	vic_spi0_hold_n	voc_spi1_clk	dg1
GPIOB_04	vic_clk_out	qdec_x_a	dbg_clk	dbg_out_14	dbg_in_14	guard	bt_trigger	modem_debug0	aud_dmic_clk			rfbd_dbg_port_28	wf_la_data4	vic_spi0_cs_n	voc_spi1_wp_n	db0
GPIOB_05	vic_pixel_data9	qdec_x_b	dbg_clk	dbg_out_15	dbg_in_15	guard	bt_fifoctrl_start	modem_debug1	aud_dmic0_in			rfbd_dbg_port_29	wf_la_data5	vic_spi0_miso	voc_spi1_cs_n	db1
GPIOB_06	vic_pixel_data10	qdec_y_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug2	aud_dmic_clk		usb_bist_ok	rfbd_dbg_port_30	wf_la_data6	vic_spi0_mosi	voc_spi1_miso	
GPIOB_07	vic_pixel_data11	qdec_y_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_fifoctrl_start	modem_debug3	aud_dmic0_in			rfbd_dbg_port_31	wf_la_data7	vic_spi0_clk	voc_spi1_mosi	
GPIOB_08	vic_v_sync	qdec_y_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug4	aud_dmic_clk				wf_la_data0			
GPIOB_09	vic_pixel_clk	qdec_y_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_fifoctrl_start	modem_debug5	aud_dmic0_in				wf_la_data1			
USB DP																
USB_DM																

ana_lomux			
PAD Name	AON Function	AON_ANA_SEL[pad_aon_gpiob_<ARRAY_INDEX>_ana_sel]	
		0	1
GPIOB_02	Function 3	GPADC_VIN<0>	PAD_CTP<0>
GPIOB_03	Function 3	GPADC_VIN<1>	PAD_CTP<1>
GPIOB_04	Function 3	GPADC_VIN<2>	PAD_CTP<2>
GPIOB_05	Function 3	GPADC_VIN<3>	PAD_CTP<3>
GPIOB_06	Function 3	GPADC_VIN<4>	PAD_CTP<4>
GPIOB_07	Function 3	GPADC_VIN<5>	PAD_CTP<5>

aon_lomux						
PAD Name	Function Mode					
	Function 0	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6
GPIOB_00	aon_dbg_out_0	aon_gpio_wkup_0	aon_gpio_out_0		bt_wakeup_ext	normal_gpiob_00	xtal_32k_in
GPIOB_01	aon_dbg_out_1	aon_gpio_wkup_1	aon_gpio_out_1		bt_wakeup_ext	normal_gpiob_01	xtal_32k_in
GPIOB_02	aon_dbg_out_2	aon_gpio_wkup_2	aon_gpio_out_2	ana_gpiob_02	bt_wakeup_ext	normal_gpiob_02	xtal_32k_in
GPIOB_03	aon_dbg_out_3	aon_gpio_wkup_3	aon_gpio_out_3	ana_gpiob_03	bt_wakeup_ext	normal_gpiob_03	xtal_32k_in
GPIOB_04	aon_dbg_out_4	aon_gpio_wkup_4	aon_gpio_out_4	ana_gpiob_04	bt_wakeup_ext	normal_gpiob_04	xtal_32k_in
GPIOB_05	aon_dbg_out_5	aon_gpio_wkup_5	aon_gpio_out_5	ana_gpiob_05	bt_wakeup_ext	normal_gpiob_05	xtal_32k_in
GPIOB_06	aon_dbg_out_6	aon_gpio_wkup_6	aon_gpio_out_6	ana_gpiob_06	bt_wakeup_ext	normal_gpiob_06	xtal_32k_in
GPIOB_07	aon_qaio_wkup_7	aon_dbg_out_7	aon_gpio_out_7	ana_gpiob_07	bt_wakeup_ext	normal_gpiob_07	xtal_32k_in
GPIOB_08	aon_dbg_out_8	aon_gpio_wkup_8	aon_gpio_out_8		bt_wakeup_ext	normal_gpiob_08	xtal_32k_in
GPIOB_09	aon_dbg_out_9	aon_gpio_wkup_9	aon_gpio_out_9		bt_wakeup_ext	normal_gpiob_09	xtal_32k_in

core_lomux (1)															
PAD Name	Function Mode														
	Function 0	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6	Function 7	Function 8	Function 9	Function 10	Function 11	Function 12	Function 13	Function 14	Function 15
GPIOA_00	jtag_tck	gpio0_00	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2s0_sd	i2s0_bck	i2s1_bck	gpt_pause	gpt_pwm_0	v_datout	smid_clk	
GPIOA_01	jtag_tms	gpio0_01	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2s0_ws	i2s0_lrck	i2s1_lrck	gpt_clk_t1	gpt_pwm_1	v_datin	smid_cmd	
GPIOA_02	gpio0_02	flashio_cs3_n	uart0_rxd	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	v_datout	smid_dat0	sdioh_dat3
GPIOA_03	gpio0_03	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	v_datin	smid_dat1	sdioh_dat2
GPIOA_04	gpio0_04	jtag_tck	uart0_rxd	uart1_rxd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2s0_sd	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	v_datout	smid_dat2	sdioh_dat1
GPIOA_05	gpio0_05	jtag_tms	uart0_txd	uart1_txd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2s0_ws	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	v_datin	smid_dat3	sdioh_dat0
GPIOA_06	gpio0_06	jtag_tdi	uart0_rxd	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	v_datout	smid_cmd	sdioh_clk
GPIOA_07	gpio0_07	jtag_tdo	uart0_rxd	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	v_datin	smid_clk	sdioh_cmd
GPIOA_08	gpio0_08	uart0_txd	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pwm_0	v_datout	smid_dat0	sdioh_dat3	
GPIOA_09	jtag_tdo	gpio0_09	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_1	v_datin	smid_dat1	sdioh_dat2	
GPIOA_10	gpio0_10	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	v_datout	smid_dat2	sdioh_cd
GPIOA_11	gpio0_11	flashio_cs3_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	v_datin	smid_dat3	sdioh_wp
GPIOA_12	gpio0_12	jtag_tck	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	v_datout	smid_cmd	
GPIOA_13	gpio0_13	jtag_tms	uart0_txd	uart1_cts_n	uart2_cts_n	spi0_miso	spi1_hold_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	v_datin	smid_clk	sdioh_wp
GPIOA_14	jtag_tdi	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	v_datout	smid_dat0		
GPIOA_15	jtag_tdo	uart0_rts_n	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	v_datin	smid_dat1		
GPIOA_16	flashio_cs1_n	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pause	v_datout	smid_dat2		
GPIOA_17	flashio_cs3_n	uart0_rxd	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_clk_t1	v_datin	smid_dat3		
GPIOA_18	jtag_tck	uart0_rts_n	uart1_rts_n	uart2_rts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_t1	gpt_pause	v_datout	smid_cmd		
GPIOA_19	jtag_tms	flashio_wp_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_t1	v_datin	smid_clk	
GPIOA_20	jtag_tdi	flashio_miso	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_0	gpt_pwm_0	v_datout	smid_dat0	
GPIOA_21	gpio0_21	flashio_cs0_n	uart0_txd	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_1	gpt_pwm_1	v_datin	smid_dat1	
GPIOA_22	gpio0_22	flashio_cs0_n	uart0_rxd	uart1_txd	uart2_txd	spi0_cs_n	spi1_cs_n	spi2_cs_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat					
GPIOA_23	gpio0_23	flashio_clk				spi0_miso	spi1_miso	spi2_miso	i2c0_scl	i2s0_dacdat	i2s1_dacdat					
GPIOA_24	gpio0_24	flashio_hold_n				spi0_mosi	spi1_mosi	spi2_wp_n	i2c1_scl	i2s0_bck	i2s1_bck					
GPIOA_25	gpio0_25					spi0_clk	spi1_clk	spi2_clk	i2c1_sda	i2s0_lrck	i2s1_lrck					
GPIOA_26	gpio0_26					spi0_wp_n	spi1_wp_n	spi2_mosi	i2c0_scl	i2s0_adcdat	i2s1_adcdat					
GPIOA_27	gpio0_27					spi0_hold_n	spi1_hold_n	spi2_hold_n	i2c0_sda	i2s0_dacdat	i2s1_dacdat					
GPIOA_28	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_sda	i2s0_bck	i2s1_bck	gpt_clk_t1	gpt_pause	v_datout	smid_dat2		
GPIOA_29	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_clk_t1	v_datin	smid_dat3		
GPIOA_30	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_0	v_datout	smid_clk		
GPIOA_31	gpio0_31	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_1	v_datin	smid_cmd	
GPIOB_00	gpio1_00	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_2	v_datout	smid_ntrn_in	sdioh_io_ntrn
GPIOB_01	gpio1_01	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_3	v_datin	smid_ntrn_in	sdioh_io_ntrn
GPIOB_02	gpio1_02	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_4	v_datout	smid_dat2	
GPIOB_03	gpio1_03	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_5	v_datin	smid_dat3	
GPIOB_04	gpio1_04	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pwm_6	v_datout	smid_dat4	
GPIOB_05	gpio1_05	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_7	v_datin	smid_dat5	
GPIOB_06	gpio1_06	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_pause	gpt_clk_t1	v_datout	smid_dat6	
GPIOB_07	gpio1_07	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_t1	v_datin	smid_dat7	
GPIOB_08	gpio1_08	flashio_cs1_n	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bck	i2s1_bck	gpt_clk_t1	gpt_pwm_0	v_datout	smid_clk	
GPIOB_09	gpio1_09	flashio_cs3_n	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_pwm_1	v_datin	smid_cmd	
FLASH_CS_N	flashio_cs0_n	sdioh_clk														
FLASH_MISO	flashio_miso	sdioh_dat0														
FLASH_WP_N	flashio_wp_n	sdioh_dat1														
FLASH_HOLD_N	flashio_hold_n	sdioh_dat2														
FLASH_CLK	flashio_clk	sdioh_dat3														
FLASH_MOSI	flashio_mosi	sdioh_cmd														

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PAD Name	Function Mode														
	Function 16	Function 17	Function 18	Function 19	Function 20	Function 21	Function 22	Function 23	Function 24	Function 25	Function 26	Function 27	Function 28	Function 29	Function 30	Function 31
GPIOA_00	vic_pixel_data8	qdec_x_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug0	aud_dmic_clk				wf_la_data0	vic_spi0_cs_n	vic_spi1_wp_n	
GPIOA_01	vic_pixel_data9	qdec_x_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_fifodrt_start	modem_debug1	aud_dmic0_in				wf_la_data1	vic_spi0_miso	vic_spi1_cs_n	
GPIOA_02	vic_pixel_data10	qdec_y_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug2	aud_dmic_clk	uabphy_scan_clk	uab_ponrst	rfbd_dbg_port_0	wf_la_data18	vic_spi0_mosi	vic_spi1_miso	
GPIOA_03	vic_pixel_data11	qdec_y_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_fifodrt_start	modem_debug3	aud_dmic_clk	uabphy_scan_nt	uab_vcontrol0	rfbd_dbg_port_1	wf_la_data19	vic_spi0_clk	vic_spi1_mosi	
GPIOA_04	vic_pixel_data0	qdec_z_a	dbg_clk	dbg_out_4	dbg_in_4	guard	bt_trigger	modem_debug4	aud_dmic0_in	uabphy_scan_se	uab_vcontrol1	rfbd_dbg_port_2	wf_la_data0	vic_spi0_cs_n	vic_spi1_clk	hsync
GPIOA_05	vic_pixel_data1	qdec_z_b	dbg_clk	dbg_out_5	dbg_in_5	guard	bt_fifodrt_start	modem_debug5	aud_dmic0_in	uabphy_scan_si1	uab_vcontrol2	rfbd_dbg_port_3	wf_la_data1	vic_spi0_cs_n	vic_spi1_cs_n	vsync
GPIOA_06	vic_pixel_data2	qdec_x_a	dbg_clk	dbg_out_6	dbg_in_6	guard	bt_trigger	modem_debug6	aud_dmic_clk	uabphy_scan_si2	uab_vcontrol3	rfbd_dbg_port_4	wf_la_data2	vic_spi0_mosi	vic_spi1_mosi	clk_rgb_out
GPIOA_07	vic_pixel_data3	qdec_x_b	dbg_clk	dbg_out_7	dbg_in_7	guard	bt_fifodrt_start	modem_debug7	aud_dmic0_in	uabphy_scan_si3	uab_corecl0in	rfbd_dbg_port_5	wf_la_data3	vic_spi0_mosi	vic_spi1_mosi	de
GPIOA_08	vic_h_sync	qdec_y_a	dbg_clk	dbg_out_8	dbg_in_8	guard	bt_trigger	modem_debug8	aud_dmic_clk	uabphy_scan_si4	uab_utmi_linestate0	rfbd_dbg_port_6	wf_la_data4	vic_spi0_hold_n	vic_spi1_hold_n	dr2
GPIOA_09	vic_v_sync	qdec_y_b	dbg_clk	dbg_out_9	dbg_in_9	guard	bt_fifodrt_start	modem_debug9	aud_dmic0_in	uabphy_scan_so1	uab_utmi_linestate1	rfbd_dbg_port_7	wf_la_data5	vic_spi0_wp_n	vic_spi1_wp_n	dr3
GPIOA_10	vic_h_sync	qdec_z_a	dbg_clk	dbg_out_10	dbg_in_10	guard	bt_trigger	modem_debug10	aud_dmic_clk	uabphy_scan_so2		rfbd_dbg_port_8	wf_la_data6	vic_spi0_cs_n	vic_spi1_cs_n	dr4
GPIOA_11	vic_v_sync	qdec_z_b	dbg_clk	dbg_out_11	dbg_in_11	guard	bt_fifodrt_start	modem_debug11	aud_dmic0_in			rfbd_dbg_port_9	wf_la_data7	vic_spi0_cs_n	vic_spi1_clk	dr5
GPIOA_12	vic_pixel_clk	qdec_x_a	dbg_clk	dbg_out_12	dbg_in_12	guard	bt_trigger	modem_debug12	aud_dmic_clk	uabphy_scan_so3		rfbd_dbg_port_10	wf_la_data8	vic_spi0_mosi	vic_spi1_mosi	dr6
GPIOA_13	vic_pixel_data4	qdec_x_b	dbg_clk	dbg_out_13	dbg_in_13	guard	bt_fifodrt_start	modem_debug13	aud_dmic0_in	uabphy_scan_so4		rfbd_dbg_port_11	wf_la_data9	vic_spi0_mosi	vic_spi1_cs_n	dr7
GPIOA_14	vic_pixel_data5	qdec_y_a	dbg_clk	dbg_out_14	dbg_in_14	guard	bt_trigger	modem_debug14	aud_dmic_clk			rfbd_dbg_port_12	wf_la_data10	vic_spi0_hold_n	vic_spi1_mosi	dg2
GPIOA_15	vic_pixel_data6	qdec_y_b	dbg_clk	dbg_out_15	dbg_in_15	guard	bt_fifodrt_start	modem_debug15	aud_dmic0_in			rfbd_dbg_port_13	wf_la_data11	vic_spi0_wp_n	vic_spi1_mosi	dg3
GPIOA_16	vic_pixel_data7	qdec_z_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug16	aud_dmic_clk			rfbd_dbg_port_14	wf_la_data12	vic_spi0_clk	vic_spi1_hold_n	dg4
GPIOA_17	vic_pixel_data8	qdec_z_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_fifodrt_start	modem_debug17	aud_dmic0_in			rfbd_dbg_port_15	wf_la_data13	vic_spi0_cs_n	vic_spi1_clk	dg5
GPIOA_18	vic_pixel_data0	qdec_x_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug18	aud_dmic_clk			rfbd_dbg_port_16	wf_la_data14	vic_spi0_miso	vic_spi1_wp_n	dg6
GPIOA_19	vic_pixel_data9	qdec_x_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_fifodrt_start	modem_debug19	aud_dmic0_in			rfbd_dbg_port_17	wf_la_data15	vic_spi0_miso	vic_spi1_clk	dg7
GPIOA_20	vic_pixel_data10	qdec_y_a	dbg_clk	dbg_out_4	dbg_in_4	guard	bt_trigger	modem_debug20	aud_dmic_clk			rfbd_dbg_port_18	wf_la_data16	vic_spi0_hold_n	vic_spi1_miso	db2
GPIOA_21	vic_pixel_data11	qdec_y_b	dbg_clk	dbg_out_5	dbg_in_5	guard	bt_fifodrt_start	modem_debug21	aud_dmic0_in			rfbd_dbg_port_19	wf_la_data17	vic_spi0_wp_n	vic_spi1_cs_n	db3
GPIOA_22	vic_pixel_data1					guard							wf_la_data18	vic_spi0_clk	vic_spi1_clk	db4
GPIOA_23	vic_pixel_data2					guard							wf_la_data19	vic_spi0_cs_n	vic_spi1_mosi	db5
GPIOA_24	vic_pixel_data3					guard								vic_spi0_miso	vic_spi1_miso	db6
GPIOA_25	vic_clk_out					guard								vic_spi0_mosi	vic_spi1_wp_n	db7
GPIOA_26	vic_clk_out					ana_gpioa_26								vic_spi0_hold_n	vic_spi1_hold_n	
GPIOA_27						ana_gpioa_27										
GPIOA_28	vic_pixel_data0	qdec_z_a	dbg_clk	dbg_out_6	dbg_in_6	guard	bt_trigger	modem_debug18	aud_dmic_clk			rfbd_dbg_port_20	wf_la_data20	vic_spi0_wp_n	vic_spi1_hold_n	
GPIOA_29	vic_pixel_data1	qdec_z_b	dbg_clk	dbg_out_7	dbg_in_7	guard	bt_fifodrt_start	modem_debug19	aud_dmic0_in			rfbd_dbg_port_21	wf_la_data21	vic_spi0_hold_n	vic_spi1_clk	
GPIOA_30	vic_pixel_data2	qdec_x_a	dbg_clk	dbg_out_8	dbg_in_8	guard	bt_trigger	modem_debug20	aud_dmic_clk			rfbd_dbg_port_22	wf_la_data22	vic_spi0_cs_n	vic_spi1_wp_n	
GPIOA_31	vic_pixel_data3	qdec_x_b	dbg_clk	dbg_out_9	dbg_in_9	guard	bt_fifodrt_start	modem_debug21	aud_dmic0_in			rfbd_dbg_port_23	wf_la_data23	vic_spi0_miso	vic_spi1_miso	
GPIOB_00	vic_h_sync	qdec_y_a	dbg_clk	dbg_out_10	dbg_in_10	guard	bt_trigger	modem_debug22	aud_dmic_clk			rfbd_dbg_port_24	wf_la_data18	vic_spi0_mosi	vic_spi1_miso	dr0
GPIOB_01	vic_pixel_clk	qdec_y_b	dbg_clk	dbg_out_11	dbg_in_11	guard	bt_fifodrt_start	modem_debug23	aud_dmic0_in			rfbd_dbg_port_25	wf_la_data19	vic_spi0_clk	vic_spi1_mosi	dr1
GPIOB_02	vic_h_sync	qdec_z_a	dbg_clk	dbg_out_12	dbg_in_12	guard	bt_trigger	modem_debug24	aud_dmic_clk			rfbd_dbg_port_26	wf_la_data2	vic_spi0_wp_n	vic_spi1_hold_n	dg2
GPIOB_03	vic_v_sync	qdec_z_b	dbg_clk	dbg_out_13	dbg_in_13	guard	bt_fifodrt_start	modem_debug25	aud_dmic0_in			rfbd_dbg_port_27	wf_la_data3	vic_spi0_hold_n	vic_spi1_clk	dg1
GPIOB_04	vic_clk_out	qdec_x_a	dbg_clk	dbg_out_14	dbg_in_14	guard	bt_trigger	modem_debug26	aud_dmic_clk			rfbd_dbg_port_28	wf_la_data4	vic_spi0_cs_n	vic_spi1_wp_n	db0
GPIOB_05	vic_pixel_data8	qdec_x_b	dbg_clk	dbg_out_15	dbg_in_15	guard	bt_fifodrt_start	modem_debug27	aud_dmic0_in			rfbd_dbg_port_29	wf_la_data5	vic_spi0_miso	vic_spi1_cs_n	db1
GPIOB_06	vic_pixel_data10	qdec_y_a	dbg_clk	dbg_out_0	dbg_in_0	guard	bt_trigger	modem_debug28	aud_dmic_clk	usb_bist_ok		rfbd_dbg_port_30	wf_la_data6	vic_spi0_mosi	vic_spi1_miso	
GPIOB_07	vic_pixel_data11	qdec_y_b	dbg_clk	dbg_out_1	dbg_in_1	guard	bt_fifodrt_start	modem_debug29	aud_dmic0_in			rfbd_dbg_port_31	wf_la_data7	vic_spi0_clk	vic_spi1_mosi	
GPIOB_08	vic_v_sync	qdec_y_a	dbg_clk	dbg_out_2	dbg_in_2	guard	bt_trigger	modem_debug30	aud_dmic_clk				wf_la_data0			
GPIOB_09	vic_pixel_clk	qdec_y_b	dbg_clk	dbg_out_3	dbg_in_3	guard	bt_fifodrt_start	modem_debug31	aud_dmic0_in				wf_la_data1			
FLASH_CS_N																
FLASH_MISO																
FLASH_WP_N																
FLASH_HOLD_N																
FLASH_CLK																
FLASH_MOSI																

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PAD Name	Function Mode														
	Function 0	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6	Function 7	Function 8	Function 9	Function 10	Function 11	Function 12	Function 13	Function 14	Function 15
GPIOA_00	jtag_tck	gpio0_00	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2s0_sd	i2s0_bck	i2s1_bck	gpt_pause	gpt_pwm_0	v_datout	smid_clk	
GPIOA_01	jtag_tms	gpio0_01	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2s0_ws	i2s0_lrck	i2s1_lrck	gpt_clk_t1	gpt_pwm_1	v_datin	smid_cmd	
GPIOA_02	gpio0_02	flashio_cs3_n	uart0_rxd	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	v_datout	smid_dat0	sdioh_dat3
GPIOA_03	gpio0_03	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	v_datin	smid_dat1	sdioh_dat2
GPIOA_04	gpio0_04	jtag_tck	uart0_rxd	uart1_rxd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2s0_sd	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	v_datout	smid_dat2	sdioh_dat1
GPIOA_05	gpio0_05	jtag_tms	uart0_txd	uart1_txd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2s0_ws	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	v_datin	smid_dat3	sdioh_dat0
GPIOA_06	gpio0_06	jtag_tdi	uart0_rxd	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	v_datout	smid_cmd	sdioh_clk
GPIOA_07	gpio0_07	jtag_tdo	uart0_rxd	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	v_datin	smid_clk	sdioh_cmd
GPIOA_08	gpio0_08	uart0_txd	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pwm_0	v_datout	smid_dat0	sdioh_dat3	
GPIOA_09	jtag_tdo	gpio0_09	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_1	v_datin	smid_dat1	sdioh_dat2	
GPIOA_10	gpio0_10	flashio_cs1_n	uart0_rxd	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	v_datout	smid_dat2	sdioh_cd
GPIOA_11	gpio0_11	flashio_cs3_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	v_datin	smid_dat3	sdioh_wp
GPIOA_12	gpio0_12	jtag_tck	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	v_datout	smid_cmd	sdioh_cd
GPIOA_13	gpio0_13	jtag_tms	uart0_txd	uart1_cts_n	uart2_cts_n	spi0_miso	spi1_hold_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	v_datin	smid_clk	sdioh_wp
GPIOA_14	gpio0_14	jtag_tdi	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	v_datout	smid_dat0	
GPIOA_15	gpio0_15	jtag_tdo	uart0_rts_n	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	v_datin	smid_dat1	
GPIOA_16	gpio0_16	flashio_cs1_n	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pause	v_datout	smid_dat2	
GPIOA_17	gpio0_17	flashio_cs3_n	uart0_rxd	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_clk_t1	v_datin	smid_dat3	
GPIOA_18	gpio0_18	flashio_cs1_n	uart0_rts_n	uart1_rts_n	uart2_rts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_t1	gpt_pause	v_datout	smid_cmd	
GPIOA_19	gpio0_19	flashio_wp_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_t1	v_datin	smid_clk	
GPIOA_20	gpio0_20	flashio_miso	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_

core_iomux (1)																
	Function Mode															
PAD Name	Function 0	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6	Function 7	Function 8	Function 9	Function 10	Function 11	Function 12	Function 13	Function 14	Function 15
GPIOA_00	jtag_tck	gpio0_00	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bck	i2s1_bck	gpt_pause	gpt_pwm_0	ir_datout	smid_clk	
GPIOA_01	jtag_tms	gpio0_01	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_clk_t1	gpt_pwm_1	ir_datin	smid_cmd	
GPIOA_02	gpio0_02	flashio_cs1_n	uart0_rxd	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	ir_datout	smid_dat0	sdioh_dat3
GPIOA_03	gpio0_03	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	ir_datin	smid_dat1	sdioh_dat2
GPIOA_04	gpio0_04	jtag_tck	uart0_rxd	uart1_txd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	ir_datout	smid_dat2	sdioh_dat1
GPIOA_05	gpio0_05	jtag_tms	uart0_txd	uart1_rxd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	ir_datin	smid_dat3	sdioh_dat0
GPIOA_06	gpio0_06	jtag_tdi	uart0_rxd	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	ir_datout	smid_cmd	sdioh_clk
GPIOA_07	gpio0_07	jtag_tdo	uart0_rxd	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	ir_datin	smid_clk	sdioh_cmd
GPIOA_08	jtag_tdi	gpio0_08	uart0_txd	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pwm_0	ir_datout	smid_dat0	sdioh_dat3
GPIOA_09	jtag_tdo	gpio0_09	uart0_rxd	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_1	ir_datin	smid_dat1	sdioh_dat2
GPIOA_10	gpio0_10	flashio_cs1_n	uart0_txd	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_2	ir_datout	smid_dat2	sdioh_cd
GPIOA_11	gpio0_11	flashio_cs1_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_3	ir_datin	smid_dat3	sdioh_wp
GPIOA_12	gpio0_12	jtag_tck	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_4	ir_datout	smid_cmd	sdioh_cd
GPIOA_13	gpio0_13	jtag_tms	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_5	ir_datin	smid_clk	sdioh_wp
GPIOA_14	gpio0_14	jtag_tdi	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_scl	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_6	ir_datout	smid_dat0	
GPIOA_15	gpio0_15	jtag_tdo	uart0_rts_n	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_sda	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_7	ir_datin	smid_dat1	
GPIOA_16	gpio0_16	flashio_cs1_n	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pause	ir_datout	smid_dat2	
GPIOA_17	gpio0_17	flashio_cs1_n	uart0_rxd	uart1_txd	uart2_txd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_clk_t1	ir_datin	smid_dat3	
GPIOA_18	gpio0_18	flashio_cs1_n	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_t1	gpt_pause	ir_datout	smid_cmd	
GPIOA_19	gpio0_19	flashio_wp_n	uart0_rts_n	uart1_txd	uart2_txd	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_t1	ir_datin	smid_clk	
GPIOA_20	gpio0_20	flashio_miso	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_mosi	spi1_miso	spi2_clk	i2c0_scl	i2s0_bck	i2s1_bck	gpt_trigger_0	gpt_pwm_0	ir_datout	smid_dat0	
GPIOA_21	gpio0_21	flashio_cs2_n	uart0_rxd	uart1_txd	uart2_txd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_sda	i2s0_lrck	i2s1_lrck	gpt_trigger_1	gpt_pwm_1	ir_datin	smid_dat1	
GPIOA_22	gpio0_22	flashio_mosi				spi0_cs_n	spi1_cs_n	spi2_cs_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat					
GPIOA_23	gpio0_23	flashio_clk				spi0_miso	spi1_miso	spi2_miso	i2c0_scl	i2s0_dacdat	i2s1_dacdat					
GPIOA_24	gpio0_24	flashio_hold_n				spi0_mosi	spi1_mosi	spi2_wp_n	i2c1_scl	i2s0_bck	i2s1_bck					
GPIOA_25	gpio0_25					spi0_clk	spi1_clk	spi2_clk	i2c1_sda	i2s0_lrck	i2s1_lrck					
GPIOA_26	gpio0_26					spi0_wp_n	spi1_wp_n	spi2_mosi	i2c0_scl	i2s0_adcdat	i2s1_adcdat					
GPIOA_27	gpio0_27					spi0_hold_n	spi1_hold_n	spi2_hold_n	i2c0_sda	i2s0_dacdat	i2s1_dacdat					
GPIOA_28	gpio0_28	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c1_sda	i2s0_bck	i2s1_bck	gpt_clk_t1	gpt_pause	ir_datout	smid_dat2	
GPIOA_29	gpio0_29	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_hold_n	spi1_clk	spi2_hold_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_clk_t1	ir_datin	smid_dat3	
GPIOA_30	gpio0_30	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_cs_n	spi1_wp_n	spi2_miso	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_0	gpt_pwm_0	ir_datout	smid_clk	
GPIOA_31	gpio0_31	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_miso	spi1_cs_n	spi2_mosi	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_1	gpt_pwm_1	ir_datin	smid_cmd	
GPIOB_00	gpio1_00	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bck	i2s1_bck	gpt_trigger_2	gpt_pwm_2	ir_datout	smid_ntn_in	sdioh_io_ntn
GPIOB_01	gpio1_01	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_3	gpt_pwm_3	ir_datin	smid_ntn_in	sdioh_io_ntn
GPIOB_02	gpio1_02	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_wp_n	spi1_hold_n	spi2_wp_n	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_trigger_4	gpt_pwm_4	ir_datout	smid_dat2	
GPIOB_03	gpio1_03	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_hold_n	spi1_clk	spi2_hold_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_trigger_5	gpt_pwm_5	ir_datin	smid_dat3	
GPIOB_04	gpio1_04	jtag_tck	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_cs_n	spi1_wp_n	spi2_miso	i2c1_sda	i2s0_bck	i2s1_bck	gpt_trigger_6	gpt_pwm_6	ir_datout	smid_dat4	
GPIOB_05	gpio1_05	jtag_tms	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_miso	spi1_cs_n	spi2_mosi	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_trigger_7	gpt_pwm_7	ir_datin	smid_dat5	
GPIOB_06	gpio1_06	jtag_tdi	uart0_rts_n	uart1_txd	uart2_txd	spi0_mosi	spi1_miso	spi2_clk	i2c0_sda	i2s0_adcdat	i2s1_adcdat	gpt_clk_t1	gpt_pause	ir_datout	smid_dat6	
GPIOB_07	gpio1_07	jtag_tdo	uart0_cts_n	uart1_rxd	uart2_rxd	spi0_clk	spi1_mosi	spi2_cs_n	i2c0_scl	i2s0_dacdat	i2s1_dacdat	gpt_pause	gpt_clk_t1	ir_datin	smid_dat7	
GPIOB_08	gpio1_08	flashio_cs1_n	uart0_rxd	uart1_cts_n	uart2_cts_n	spi0_mosi	spi1_miso	spi2_clk	i2c1_sda	i2s0_bck	i2s1_bck	gpt_clk_t1	gpt_pwm_0	ir_datout	smid_clk	
GPIOB_09	gpio1_09	flashio_cs1_n	uart0_txd	uart1_rts_n	uart2_rts_n	spi0_clk	spi1_mosi	spi2_cs_n	i2c1_scl	i2s0_lrck	i2s1_lrck	gpt_pause	gpt_pwm_1	ir_datin	smid_cmd	
FLASH_CS_N	flashio_cs0_n	sdioh_clk														
FLASH_MISO	flashio_miso	sdioh_dat0														
FLASH_WP_N	flashio_wp_n	sdioh_dat1														
FLASH_HOLD_N	flashio_hold_n	sdioh_dat2														
FLASH_CLK	flashio_clk	sdioh_dat3														
FLASH_MOSI	flashio_mosi	sdioh_cmd														